

FIG. 1

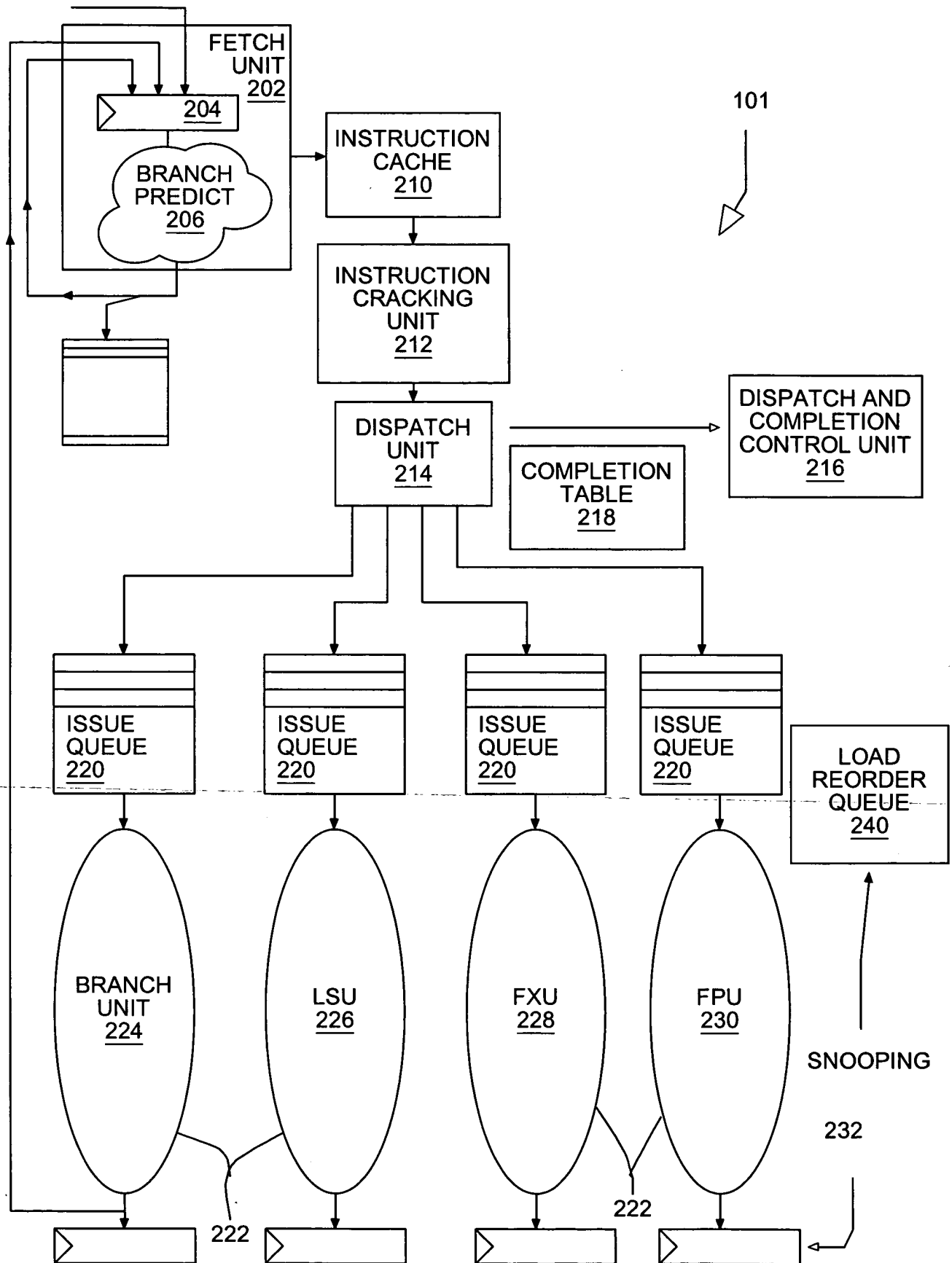


FIG. 2

LD R1, MEM(EA1)  
 LD R2, MEM(EA2)  
 IA1      CMP R1, R2  
 IA2      BC LBL1  
 IA3      INST A  
 IA4      INST B  
 IA5      INST C  
 IA6      BR LBL2  
         LBL1,  
 IA7      INST D  
 IA8      INST E  
 IA9      INST F  
         LBL2,  
 IA10     ETC

FIG. 3

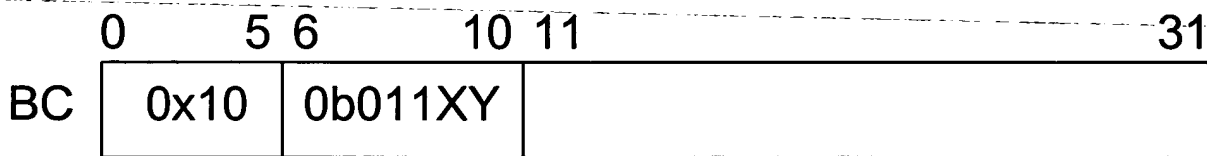


FIG. 4

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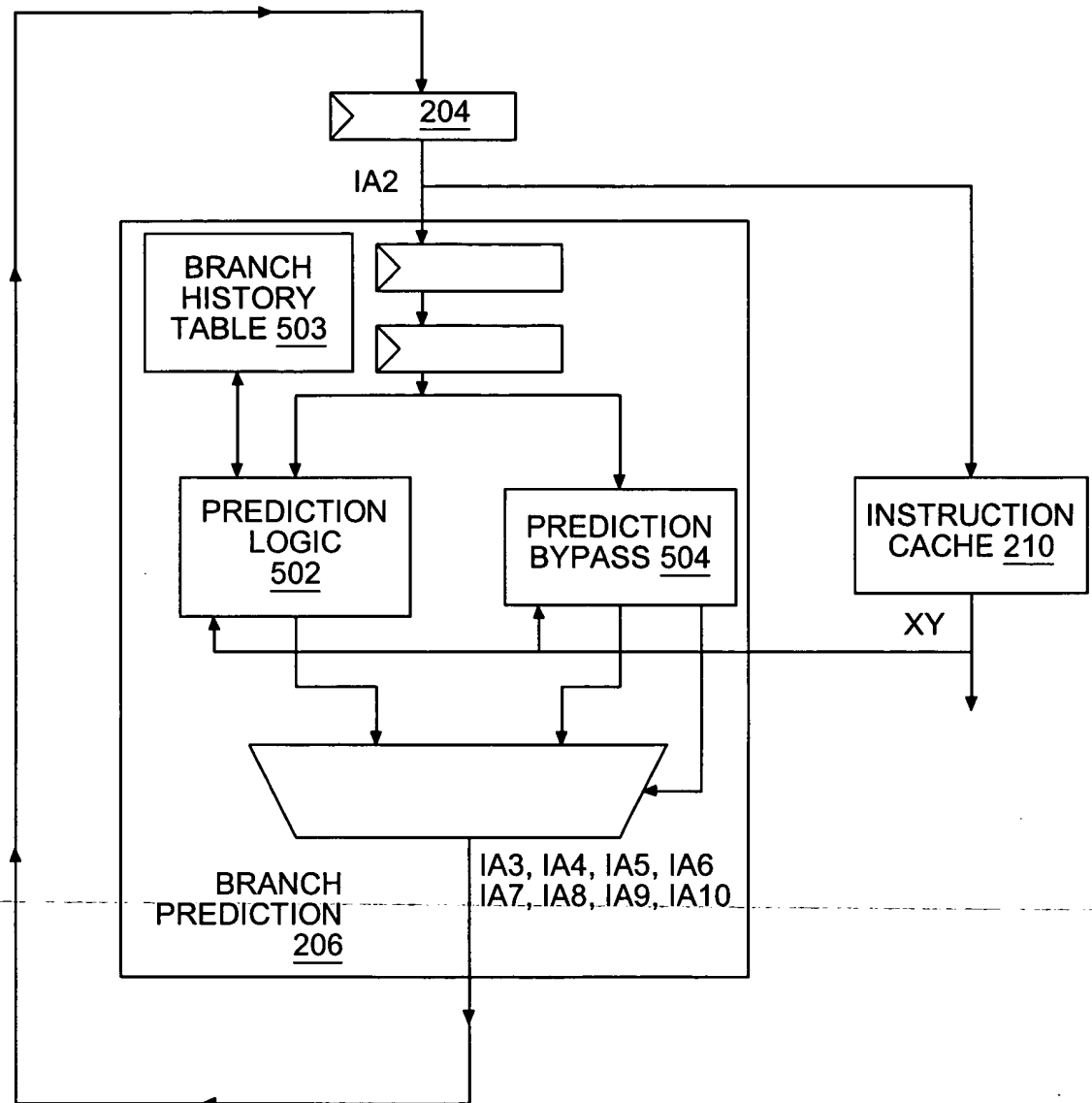


FIG. 5